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**PATENT APPLICATION**  
**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

JC997 U.S. PTO  
09/940472  
08/29/01

In re application of

Atty. Dkt.: Q65962

Katsuji KIMURA

Appln. No.

Group Art Unit: Unknown

Confirmation No.: Unknown

Examiner: Unknown

Filed: August 29, 2001

For: LINEAR VOLTAGE SUBTRACTOR/ADDER CIRCUIT AND MOS DIFFERENTIAL  
AMPLIFIER CIRCUIT THEREFOR

**INFORMATION DISCLOSURE STATEMENT**  
**UNDER 37 C.F.R. §§ 1.97 and 1.98**

Commissioner for Patents  
Washington, D.C. 20231

Sir:

In accordance with the duty of disclosure under 37 C.F.R. § 1.56, Applicant hereby  
notifies the U.S. Patent and Trademark Office of the documents which are listed on the attached  
Form PTO-1449 and/or listed herein and which the Examiner may deem material to patentability  
of the claims of the above-identified application.

1. Japanese Patent No. 2,556,293, issued September 5, 1996 (Laid-open publication No. 07-336163) with English Abstract.
2. Japanese Laid-Open Patent Publication No. 7-127887, published May 16, 1995, with English Abstract.
3. R. R. Torrance et al., "CMOS Voltage to Current Transducers", *IEEE Transactions on Circuits and Systems*, Vol. CAS-32, No. 11, November 1985, pp. 1097-1104.
4. K. Kimura, "MOS Linear and Square-Law Transconductance Amplifiers Consisting of a Source-Coupled Pair with Load Transistors and a Quandritail Cell Using Only N-Channel MOS Unit Transistors", *Technical Report of IEICE*, CAS98-41, NLP98-49, July 1998, pp. 17-24.

Katsuji Kimura  
Q65962  
Information Disclosure Statement

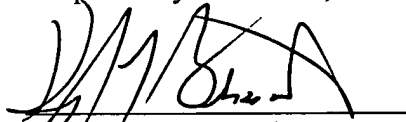
One copy of each of the listed documents is submitted herewith.

The present Information Disclosure Statement is being filed: (1) No later than three months from the application's filing date for an application other than a continued prosecution application (CPA) under §1.53(d); (2) Before the mailing date of the first Office Action on the merits (whichever is later); or (3) Before the mailing date of the first Office Action after filing a request for continued examination (RCE) under §1.114, and therefore, no Statement under 37 C.F.R. § 1.97(e) or fee under 37 C.F.R. § 1.17(p) is required.

In compliance with the concise explanation requirement under 37 C.F.R. § 1.98(a)(3) for foreign language documents, Applicant relies on the English Abstracts as the brief statements of relevance.

The submission of the listed documents is not intended as an admission that any such document constitutes prior art against the claims of the present application. Applicant does not waive any right to take any action that would be appropriate to antedate or otherwise remove any listed document as a competent reference against the claims of the present application.

Respectfully submitted,

  
Howard L. Bernstein  
Registration No. 25,665

SUGHRUE, MION, ZINN,  
MACPEAK & SEAS, PLLC  
2100 Pennsylvania Avenue, N.W.  
Washington, D.C. 20037-3213  
Telephone: (202) 293-7060  
Facsimile: (202) 293-7860

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